

In the Claims:

Please amend claims 1, 11 and 12. The status of the claims is as follows:

1. (Currently Amended) An information processing apparatus for translating a source file including dynamic variables and static variables into object files by a compiling process and converting the object files into executable load modules by a linking process, the apparatus comprising:

dynamic variable specifying means for specifying a target dynamic variable from the source file;

area specifying means for specifying areas in a memory ensured in the case of the dynamic variable specified by the dynamic variable specifying means being developed into a the memory at the time of executing the load module; and

initializing means for initializing areas specified by the area specifying means to a predetermined value

wherein the specified areas are determined by obtaining starting and ending addresses in an object file where an object code relating to the target dynamic variable is stored.

2. (Previously Presented) The information processing apparatus according to claim 1, further comprising area integrating means for integrating areas ensured in the case

of a plurality of different dynamic variables specified by the dynamic variable specifying means being developed into the memory.

3. (Original) The information processing apparatus according to claim 1, further comprising variable integrating means for integrating the same dynamic variables dispersed in one or more object files integrated by the linking process.

4. (Previously Presented) The information processing apparatus according to claim 1, wherein the dynamic variable specifying means allocates the target dynamic variable to a new data section, further wherein the initializing means performs an initializing process on a dynamic variable alone allocated to the new data section.

5. (Previously Presented) The information processing apparatus according to claim 4, wherein when a plurality of object files are linked to generate the load module, the initializing means does not perform an initializing process on the dynamic variable which is allocated to the new data section in a predetermined object file and is allocated to an ordinary data section in another object file.

6. (Original) The information processing apparatus according to claim 1, further comprising initial-value entering means for entering an initial value used by the initializing means before a compiling process.

7. (Original) The information processing apparatus according to claim 1, further comprising initial-value entering means for entering an initial value used by the initializing means before executing the load module.

8. (Previously Presented) The information processing apparatus according to claim 1, further comprising initialized variable specifying means for specifying the dynamic variable on which the initializing process is performed.

9. (Original) The information processing apparatus according to claim 1, further comprising error informing means for informing of an error in the case of a dynamic variable which holds the initial value being referred to at the time of executing the load module.

10. (Original) The information processing apparatus according to claim 9, further comprising memory ensuring means for ensuring memory areas being a predetermined number more than the number of elements of an array declared in source code if the dynamic variable is an array, wherein the initializing means initializes all areas in a memory ensured by the memory ensuring means to a predetermined value, further wherein the error informing means informs of an error in the case of an array which holds the initial value being referred to.

11. (Currently Amended) A computer-readable record medium recording a computer program for translating a source file including dynamic variables and static variables into an object file by a compiling process and converting the object file into an executable load module by a linking process, the program comprising the functions of:

specifying a target dynamic variable from the source file;

specifying areas in a memory ensured in the case of the dynamic variable specified ~~by the dynamic variable specifying means~~ being developed into a the memory at the time of executing the load module; and

initializing areas specified ~~by the area specifying means~~ to a predetermined ~~value~~value,

wherein the specified areas are determined by obtaining starting and ending addresses in an object file where an object code relating to the target dynamic variable is stored.

12. (Currently Amended) An information processing apparatus for translating a source file including an array into an object file by a compiling process and converting the object file into an executable load module by a linking process, the apparatus comprising:

array specifying means for specifying a target array from the source file;

area ensuring means for ensuring, at the time of executing the load module, an

availability of areas in a memory ~~being of~~ a predetermined number of bytes more than areas  
declared in an array specified by the array specifying means; and

initializing means for initializing areas ensured by the area ensuring means to a  
predetermined value.